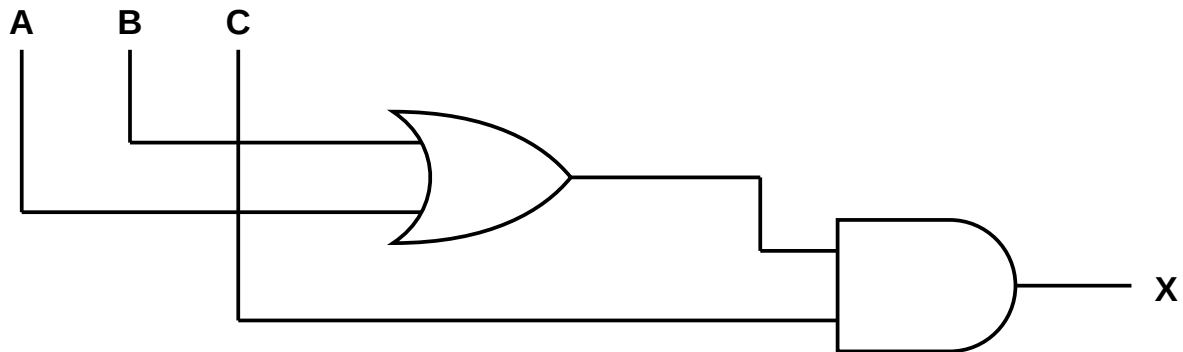


Circuit $\rightarrow$ Truth Table

Name _____ Period _____ Date _____

1. Study this circuit.



2. Complete the truth table.

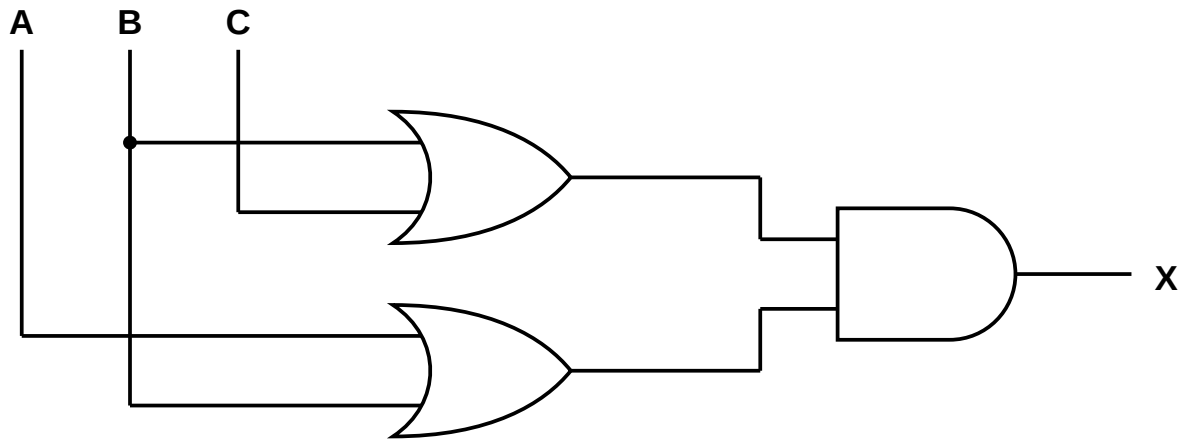
Label each blank column with the Boolean expression for one gate's output, then fill in every row.

[illegible]

Circuit $\rightarrow$ Truth Table

Name _____ Period _____ Date _____

1. Study this circuit.



2. Complete the truth table.

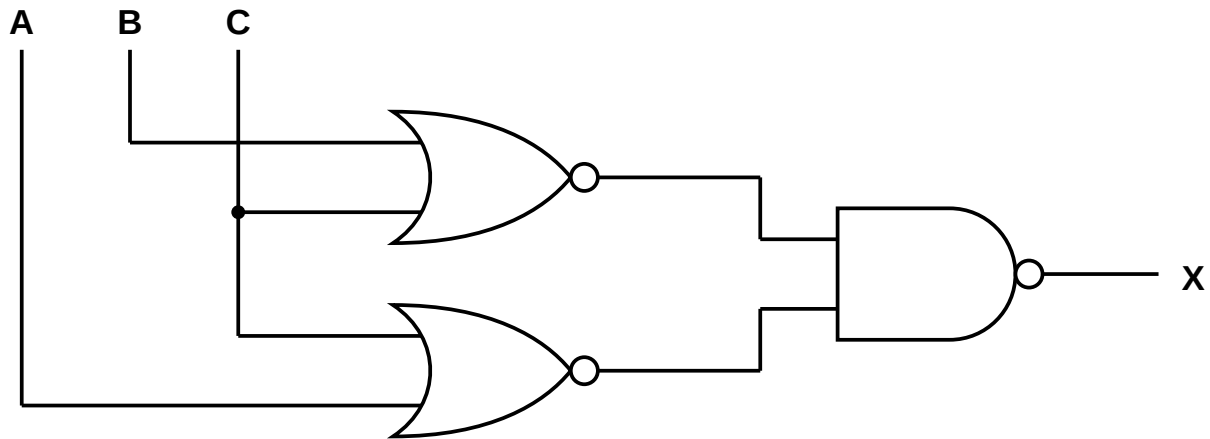
Label each blank column with the Boolean expression for one gate's output, then fill in every row.

[illegible]

Circuit $\rightarrow$ Truth Table

Name _____ Period _____ Date _____

1. Study this circuit.



2. Complete the truth table.

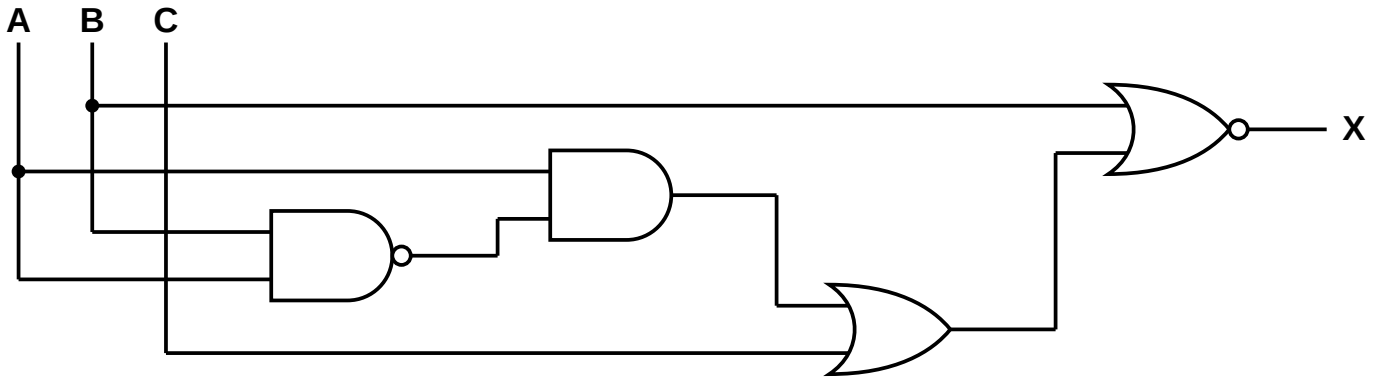
Label each blank column with the Boolean expression for one gate's output, then fill in every row.

[illegible]

Circuit $\rightarrow$ Truth Table

Name _____ Period _____ Date _____

1. Study this circuit.



2. Complete the truth table.

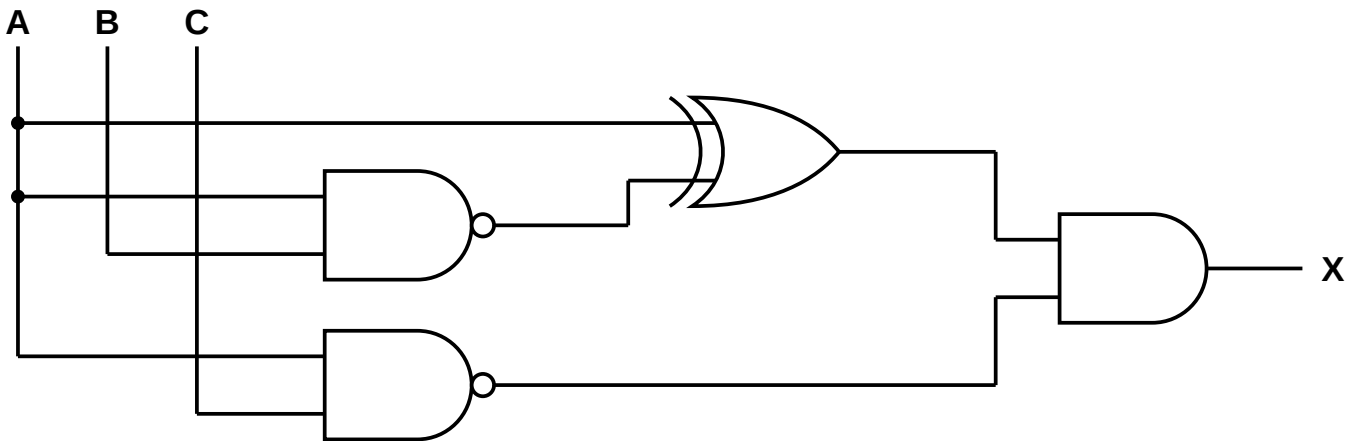
Label each blank column with the Boolean expression for one gate's output, then fill in every row.

[illegible]

Circuit $\rightarrow$ Truth Table

Name _____ Period _____ Date _____

1. Study this circuit.



2. Complete the truth table.

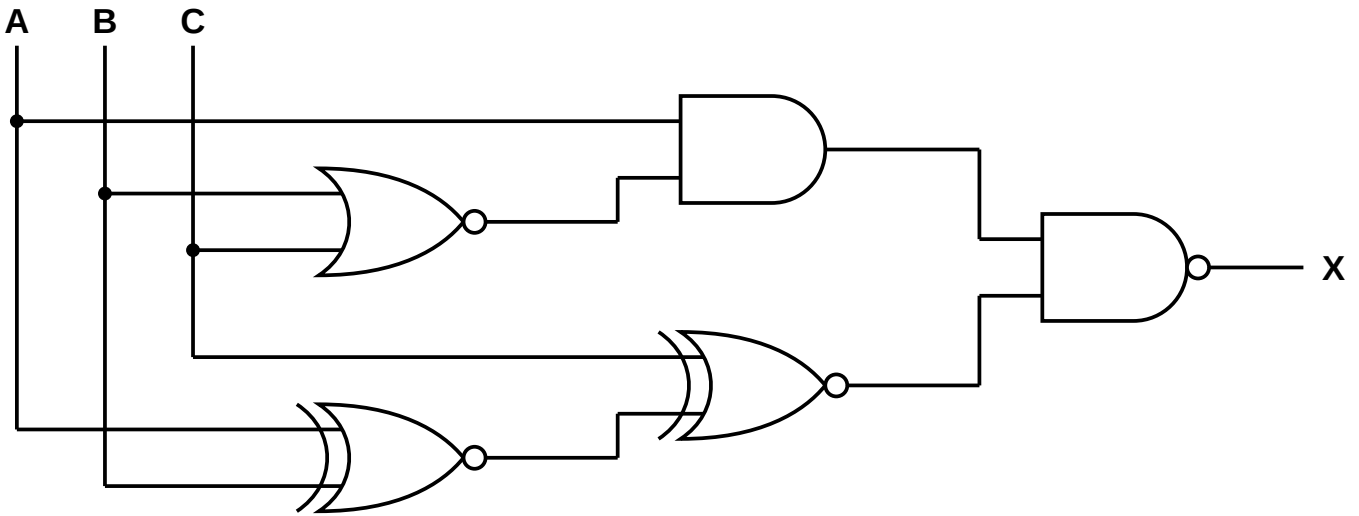
Label each blank column with the Boolean expression for one gate's output, then fill in every row.

[illegible]

Circuit $\rightarrow$ Truth Table

Name _____ Period _____ Date _____

1. Study this circuit.



2. Complete the truth table.

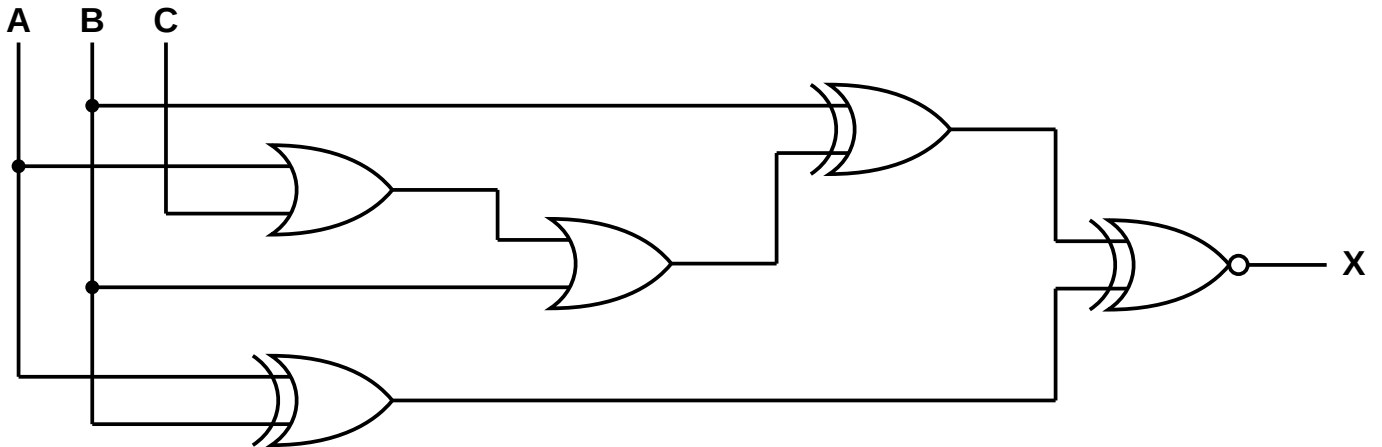
Label each blank column with the Boolean expression for one gate's output, then fill in every row.

[illegible]

Circuit $\rightarrow$ Truth Table

Name _____ Period _____ Date _____

1. Study this circuit.



2. Complete the truth table.

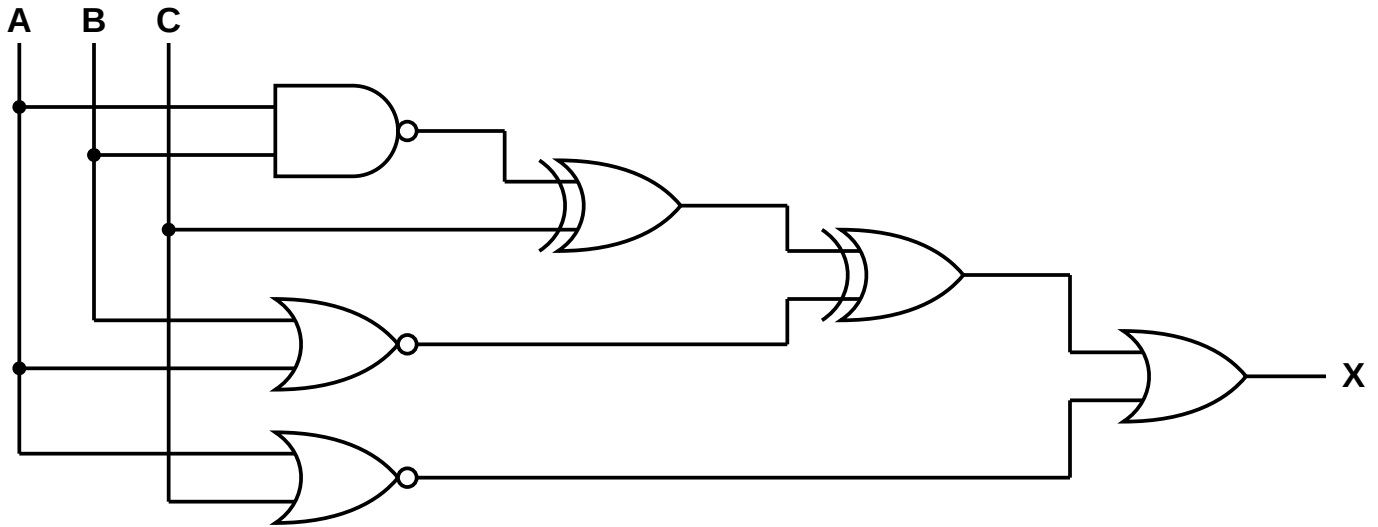
Label each blank column with the Boolean expression for one gate's output, then fill in every row.

[illegible]

Circuit $\rightarrow$ Truth Table

Name _____ Period _____ Date _____

1. Study this circuit.



2. Complete the truth table.

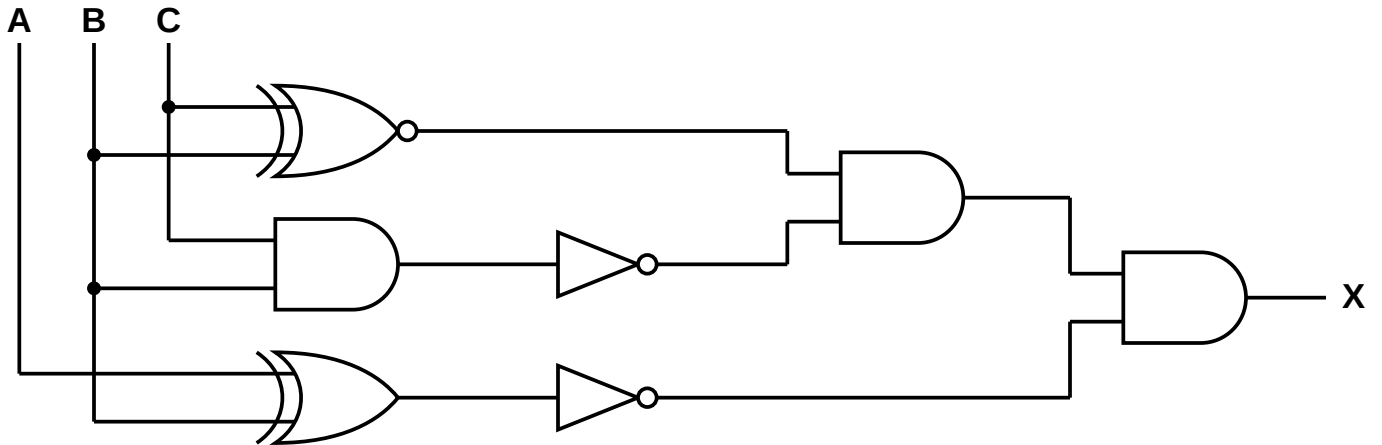
Label each blank column with the Boolean expression for one gate's output, then fill in every row.

[illegible]

Circuit $\rightarrow$ Truth Table

Name _____ Period _____ Date _____

1. Study this circuit.



2. Complete the truth table.

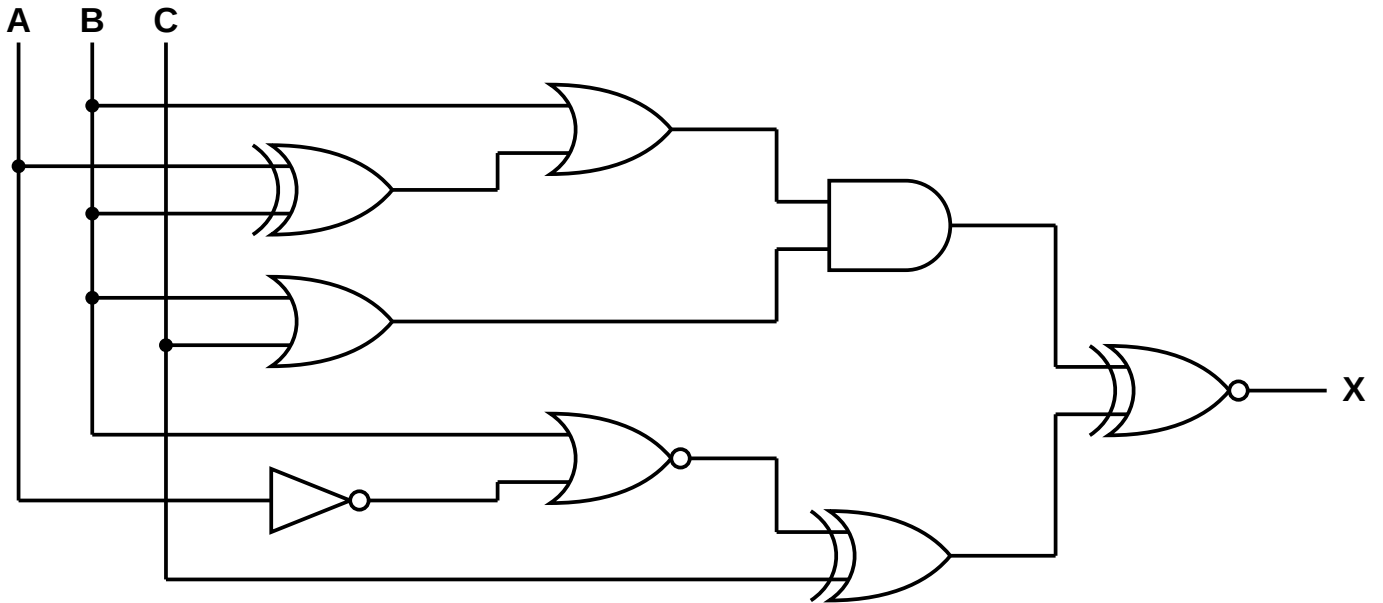
Label each blank column with the Boolean expression for one gate's output, then fill in every row.

[illegible]

Circuit $\rightarrow$ Truth Table

Name _____ Period _____ Date _____

1. Study this circuit.



2. Complete the truth table.

Label each blank column with the Boolean expression for one gate's output, then fill in every row.

[illegible]